Syllabus, Spring 2018

**CSCI 3301: Computer Organization**

**Time:** Monday and Wednesday: 5:00pm – 6:15pm

**Place:** TBA

**Prof:** Md Tamjidul HOQUE

**My Info:**
- Email: thoque@uno.edu, Phone: 504-280-2406.
- Office Hours: Monday, Wednesday: 1pm to 2pm and 6:15pm to 7:15pm, in Math 333. Friday 12noon to 2pm, in Math 333.

**Prereqs:** CSCI 2120 (Software Design and Development II) and CSCI 2450 (Machine Structure and Assembly Language Programming)


**Marks Distribution:**
- Class-Test (3), HW/Programming (4) (5 out of 7) x 13 % → 65%
- Final Examination (Must attend to pass) → 30%
- Attendance: → 5%
  - %5: [90-100%], 4%: [85-90), 3%: [80-85), 2%: [75-80), 1%: [70-75), 0%: <70.

**Grading:**
- A: [90 – 100]%, B: [80-90)%, C: [70-80)%, D: [60-70)%, F: < 60%.

**Exams:** Tests/Assignments are expected at the end of every chapters (for chapter 1 to 6).
- **Last Class:** TBA
- **Final Exam:** TBA.

**Due Dates:** Every assignment handed out will be clearly marked with a due date. You are responsible for handing in your assignment on time. Late submissions will be assessed at the following rates: 80% for 1-48 hours late, 60% for 49-96 hours late, 40% for 97-144 hours late, 20% for 145-168 hours late. Assignments that are more than a week late will receive no credit.

**Attendance:** Your attendance at class is required and essential for you to meet course requirements. Attendance will be taken at the beginning of each class. Absence from class will negatively affect your performance in the course.

**Objectives:** This course establishes relation between hardware and software. With the recent switch from uniprocessor to multicore processors the compliers writers, operating system designers, database programmers, software engineers, programmers for faster program and efficient programming need a firm understanding of processor and multicore design and performance evaluation; instruction set design and addressing having
implicit parallelism; data path design and pipelining; control structures and microprogramming; memory management, caches, and memory hierarchies; interrupts and I/O structures; parallel processing, GPU and so on. Hardware designers also need to clearly know the effect of their work on software application – and this course will help achieve these goals.

**Conduct:** Please be respectful of your classmates and refrain from disruptive activities in the classroom. Come to class on time. Turn off cell phones in the classroom. If you use a laptop or other electronic device to take notes, you must keep keyboard noise to a minimum. If you can’t type silently then you should use paper and pencil. Your cooperation is appreciated.

**Moodle:** Essential course material (including this syllabus) will be posted to the CSCI 3301 course page on Moodle (http://www.uno.edu/moodle). Important notices will occasionally be transmitted through Moodle’s email feature. Please ensure that you can access your uno.edu email.